

EAST - [default.wsp:1]

File View Edit Tools Window Help

Active

- L1: (1) nonvolatile with eeprom with (multi-bit or multibit
- L2: (9) nonvolatile same eeprom same (multi-bit or multibit
- L3: (112) nonvolatile same eeprom and (multi-bit or multibit
- L4: (48) 3 and eras\$3 and read\$3 and program\$5
- L5: (7) 4 and doped and region
- L6: (0) 4 and (binary adj status)
- L7: (13) 4 and binary and status

DBs: USPAT; US-PGPUB; EPO; IPO

Default operator: OR

Highlight all hit terms initially

	Search Text	DBs	Time Stamp
1	nonvolatile with eeprom with (multi-bit or multibit or (multi adj bit))	USPAT; US-PGPUB;	2004/09/04 10:43
2	nonvolatile same eeprom same (multi-bit or multibit or (multi adj bit))	USPAT; US-PGPUB;	2004/09/04 11:28
3	nonvolatile same eeprom and (multi-bit or multibit or (multi adj bit))	USPAT; US-PGPUB;	2004/09/04 11:28
4	3 and eras\$3 and read\$3 and program\$5	USPAT; US-PGPUB;	2004/09/04 11:29
5	4 and doped and region	USPAT; US-PGPUB;	2004/09/04 13:01
6	4 and (binary adj status)	USPAT; US-PGPUB;	2004/09/04 13:01
7	4 and binary and status	USPAT; US-PGPUB;	2004/09/04 13:03
8	(dual adj bit) same left same right same read\$3 same (program\$5 or writ\$) same eras\$3	USPAT; US-PGPUB;	2004/09/04 15:21
9	nonvolatile same multi-bit same dual-bit same l-shape same spacer	USPAT; US-PGPUB;	2004/09/04 17:13
10	nonvolatile and multi-bit and dual-bit and l-shape and spacer	USPAT; US-PGPUB;	2004/09/04 17:13
11	nonvolatile and dual-bit and l-shape and spacer	USPAT; US-PGPUB;	2004/09/04 17:13
12	nonvolatile and dual-bit and l-shape	USPAT; US-PGPUB;	2004/09/04 17:14
13	nonvolatile and (dual-bit or multi-bit) and L-shape	USPAT; US-PGPUB;	2004/09/04 18:31
14	gate and (dual-bit or multi-bit) and L-shape and charge	USPAT; US-PGPUB;	2004/09/04 18:30
15	memory and (dual-bit or multi-bit) and L-shape	USPAT; US-PGPUB;	2004/09/04 18:32
16	nonvolatile and (dual-bit or multi-bit) and current and doped and region	USPAT; US-PGPUB;	2004/09/04 18:34
17	nonvolatile and (dual-bit or multi-bit) and current and doped and region and channel and spacer	USPAT; US-PGPUB;	2004/09/04 18:34

Ready

NUM

EAST - [default.wsp:1]

FileViewEditToolsWindowHelp

Drafts

Pending

Active

- L1: (1) nonvolatile with eprom with (multi-bit or multibit
- L2: (9) nonvolatile same eprom same (multi-bit or multib

Failed

Saved

Favorites

Tagged (0)

DBs

USPAT:US-PGPUB,EPO,JPO

Default operator: OR

Highlight all hit terms initially

Search Text

DBs

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	1	nonvolatile with eprom with (multi-bit or multibit or (multi adj bit))	USPAT: US-PGPUB; 20 EPO; JPO
2	BRS	L2	9	nonvolatile same eprom same (multi-bit or multibit or (multi adj bit))	USPAT: US-PGPUB; 20 EPO; JPO

Ready

NUM

EAST - [default.wsp:1]

FileViewEditToolsWindowHelp

Pending

Active

L1: (1546) nonvolatile same program\$5 same eras\$3 same

L2: (556) nonvolatile with program\$5 with eras\$3 with re

L3: (0) 2 with second with first with doped with apply\$3

L4: (0) 2 same (second with first with doped with apply\$

L5: (0) 2 same second same first same doped same apply

L6: (4) 2 and (second same first same doped same apply\$

L7: (105) 2 and second and first and doped and apply\$3

Print

Copy

Paste

Find

Find Next

DBs

USPAT: US-PGPUB, EPO, JPO

Default operator

OR

Search

Highlight all hit items initially

BRS form

BAR form

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	1546	nonvolatile same program\$5 same eras\$3 same read\$3	USPAT: US-PGPUB; 20 EPO; JPO
2	BRS	L2	556	nonvolatile with program\$5 with eras\$3 with read\$3	USPAT: US-PGPUB; 20 EPO; JPO
3	BRS	L3	0	2 with second with first with doped with apply\$3 with (potential or bias or volt\$3)	USPAT: US-PGPUB; 20 EPO; JPO
4	BRS	L4	0	2 same (second with first with doped with apply\$3 with (potential or bias or volt\$3))	USPAT: US-PGPUB; 20 EPO; JPO
5	BRS	L5	0	2 same second same first same doped same apply\$3 same (potential or bias or volt\$3)	USPAT: US-PGPUB; 20 EPO; JPO
6	BRS	L6	4	2 and (second same first same doped same apply\$3 same (potential or bias or volt\$3))	USPAT: US-PGPUB; 20 EPO; JPO
7	BRS	L7	105	2 and second and first and doped and apply\$3 and (potential or bias or volt\$3)	USPAT: US-PGPUB; 20 EPO; JPO
8	BRS	L8	42	7 and spacer	USPAT: US-PGPUB; 20 EPO; JPO
9	BRS	L9	41	8 and inject\$3	USPAT: US-PGPUB; 20 EPO; JPO
10	BRS	L10	11	9 and binary	USPAT: US-PGPUB; 20 EPO; JPO

HTML

NUM